IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Manrique J. Brenes; Yen T. Nguyen

Assignee: Cisco Technology, Inc.

Title: METHOD AND SYSTEM FOR REAL-TIME BIT ERROR

RATIO DETERMINATION

Serial No.: 10/773,705 Filing Date; February 6, 2004

Examiner: Stephen M. Baker Group Art Unit; 2112

Docket No: CISO209US Confirmation No. 8310

Austin, Texas September 15, 2008

MAIL STOP RCE COMMISSIONER FOR PATENTS P. O. BOX 1450 ALEXANDRIA, VA 22313-1450

AMENDMENT AND REMARKS ACCOMPANYING REQUEST FOR CONTINUED EXAMINATION

Dear Sir:

This paper and the accompanying Request for Continued Examination (RCE) are being submitted in response to the Advisory Action dated June 24, 2008 and the Final Office Action dated April 15, 2008, having a shortened statutory period set to expire on July 15, 2008. Accompanying this response is a 2-month Petition Under 37 C.F.R. § 1.136 for Extension of Time setting a new time for response of September 15, 2008. Further examination and reconsideration are respectfully requested in view of the following amendments and remarks.

No Amendments to the Specification are presented in this paper.

No Amendments to the Drawings are presented in this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 10 of this paper.